

520.41109X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. MATSUMOTO, et al.  
Application No.: 10/052,513  
Filed: January 23, 2002  
For: METHOD AND SYSTEM FOR PROCESSING  
A SEMICONDUCTOR DEVICE  
Art Unit: 1756  
Examiner: C. G. Young

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RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Mail Stop Non Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


November 20, 2003

Sir:

In response to the Restriction Requirement dated April 9, 2003, Applicants hereby elect the **Group I** claims 1-16, drawn to a method of processing a semiconductor device, without traverse.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Docket No. 520.41109X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,  
ANTONELLI, TERRY, STOUT & KRAUS, LLP

By   
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GEM/dlt

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